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APPLICANTS
 Paul Murtagh, Duluth, GA;

** CONTINUING DATA *None* *****

** FOREIGN APPLICATIONS *None* *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
 ** 12/09/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <i>[Signature]</i> Examiner's Signature Initials	STATE OR COUNTRY GA	SHEETS DRAWING 3	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 3
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TITLE
 Delay-locked loop (DLL) integrated circuits having high bandwidth and reliable locking characteristics

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